

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/876290	CONT/PRIOR F	CLASS 438 25	SUBCLASS 686	ART UNIT 2812 2814	EXAMINER Alan Chan
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APPLICANTS

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TITLE

Assembly jig and manufacturing method of multilayer semiconductor device

PTO-2040
12/89

ISSUING CLASSIFICATION

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____		ISSUE FEE	
Amount Due			Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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